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# The effect of steps on the oxidation of a caesiated $Si(100)2 \times 1$ surface

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Abstract. Exposure of a caesiated Si(100)2 × 1 surface to O<sub>2</sub> and subsequent heating to about 800 °C caused the formation of SiO<sub>4</sub> and SiO<sub>2</sub>. For a saturation Cs coverage and the same O<sub>2</sub> exposure, the growth of oxides is more pronounced on a stepped than on a non-stepped Si(100)2 × 1 surface. This is due to the higher coverage of Cs on the stepped surface. When the above oxidation cycle is repeated, the SiO<sub>2</sub> predominates. After six oxidation cycles the average thickness of SiO<sub>2</sub> is about 11 Å and 16 Å on Si(100)2 × 1 surfaces without and with steps, respectively.

#### 1. Introduction

The Si–SiO<sub>2</sub> interfaces are very important in microelectronics. This is shown by the recent great interest in the oxidation of Si surfaces [1-5]. The direct oxidation of Si requires a relatively high temperature and pressure, which cause imperfections in the interface region [4].

In the last decade, there have been systematic studies on the co-adsorption of alkali and  $O_2$  on metals and semiconductors which suggest that, regardless of the substrate, the presence of Cs increases drastically the sticking coefficient and the maximum amount of  $O_2$  that can be deposited on the surface [6–8]. The presence of Cs also enhances the oxidation of some metal substrates [9]. These results suggest that Cs can be used as an additive to enhance the oxidation of Si. In fact, recent studies on the oxidation of Si surfaces suggest that the presence of Cs reduces the oxidation temperature and promotes the growth of the oxide [5, 10–12]. However, some problems exist. The thickness of the oxide is relatively small and it is not quite uniform [13]. Thus it is reasonable to look for an additional source of enhancement for the oxidation of Si surfaces besides the presence of alkali.

The steps on a Si surface might be helpful for further enhancement of the oxidation. The edge sites of the steps have more dangling bonds than do the sites on a regular Si plane [14]; this may increase the growth of Si oxide. In this work, a systematic experimental study of the oxidation of Si(100)2  $\times$  1 surfaces with and without steps in the presence of Cs has been carried out.

## 2. Experimental details

Experiments were performed in a conventional UHV chamber equipped with a hemicylindrical mirror analyser for Auger electron spectroscopy (AES), four-grid optics for low-energy electron diffraction (LEED), a quadrupole mass spectrometer for thermal desorption (TDS) and facilities for work function (WF) measurements using the electron beam diode method. Cs was evaporated from a commercial SAES-Getters source and the background pressure during deposition was kept below  $10^{-10}$  Torr. The Cs flux used in the experiments, as measured with a Pt ioniser, was  $1.0 \times 10^{12}$  atoms cm<sup>-2</sup> s<sup>-1</sup> and could be kept stable within 5% even for the largest exposures. Spectroscopically pure O<sub>2</sub> was admitted to the system through a bakeable leak valve. The partial pressure of  $O_2$  during exposure was  $5 \times 10^{-8}$  Torr. The  $O_2$  pressure was monitored with a quadrupole mass spectrometer. During the AES measurements the beam current and modulation voltage were  $2 \mu A$  and 4 V, respectively, while the background pressure was below  $10^{-10}$  Torr. Two Si crystals  $10 \text{ mm} \times 10 \text{ mm} \times 1 \text{ mm}$  in size were simultaneously mounted inside the chamber, one of them with its surface parallel to the (100) crystallographic plane within  $\pm 0.2^{\circ}$  and the other one with its surface 5° off this plane in the [110] zone. Both crystals were n type with resistivities of  $10^{-2} \Omega$  cm (plane) and  $100 \Omega$  cm (vicinal), respectively. The samples could be heated by passing current through a Ta strip uniformly pressed between the crystal and a Ta foil case. The sample temperature was measured with a (Ni-Cr)/(Ni-Al) thermocouple spot welded on the case and calibrated with an IR pyrometer.

The crystals, which had also been used in a previous study [15], were initially cleaned with successive  $Ar^+$  bombardment and annealing cycles at about 1100 K. On exposure to the atmosphere the main contaminant of both surfaces was C which could be eliminated by heating to about 1400 K. After cleaning, all contaminants were below the AES detection limit, the plane (100) surface exhibited the usual reconstructed (2 × 1) LEED pattern, while the LEED pattern of the vicinal surface showed the characteristic singledouble spot succession with varying electron energy as expected from a regularly stepped surface. Analysis of the energy dependence of the integral spot splitting led to a step height of about  $\alpha_0/2$ , where  $\alpha_0 = 5.43$  Å is the Si unit-cell parameter, and an average terrace width of about 30 Å. These values are in agreement with those found in the literature for similarly cleaned 5° vicinal Si(100) surfaces [16].

## 3. Results and discussion

## 3.1. Cs adsorption

The behaviour of Cs on Si(100)2  $\times$  1 with and without steps has been studied recently in our laboratory [17]. Briefly, the findings which are related to this paper are the following. Cs adsorbs initially on the edge sites of the steps and subsequently on the terraces. The presence of the steps increases the sticking coefficient and the maximum amount of Cs that can be deposited on Si(100)2  $\times$  1. The increase in the sticking coefficient was about 50% and was estimated from the AES curves and the measured Cs flux. This implies that the sticking coefficient of Cs on flat Si(100) is less than unity.

## 3.2. $O_2$ adsorption

Figure 1(a) shows the WF change  $\Delta \Phi$  against O<sub>2</sub> exposure on Si(100)2 × 1 with and without steps. First, we should mention that the WF value of clean stepped Si(100)2 × 1



Figure 1. (a) WF change and (b) the O(520 eV)/ Si(92 eV) Auger peak-to-peak height ratio against O<sub>2</sub> exposure on Si(100)2 × 1 with ( $\bigcirc$ ) and without (×) steps. (The Si(92 eV) peak height corresponds to the clean substrate before each measurement.)

was found to be 0.2 eV greater than that of the flat crystal. This is consistent with theoretical work which suggests that the wF of a Si(111) surface with steps is greater than that without steps [14] and agrees qualitatively with experimental data on p-type Si(111) [18]. Our experimental data in figure 1(a) show that deposition of O on the flat surface causes a small increase in the wF, while on the stepped surface the wF decreases. A possible explanation is that the centre of mass of the initially deposited O atoms near the steps lies below the top atom row of the stepped surface. Thus the resultant surface dipole moment has an outward direction which leads to the wF decrease. These small differences could also be attributed to changes in the surface band bending.

Figure 1(b) shows the O(520 eV)/Si(92 eV) Auger peak-to-peak height ratio plotted against O<sub>2</sub> exposure on Si(100)2 × 1 surfaces with and without steps. (The height of the Si(92 eV) peak is always that of the clean substrate before each measurement.) This figure indicates that both the adsorption rate and the maximum amount of O are about the same on the flat and stepped Si(100)2 × 1 surfaces.

#### 3.3. Adsorption of $O_2$ on caesiated $Si(100)2 \times 1$

3.3.1. AES measurements. Figures 2(a) and 2(b) show the ratio of O(520 eV)/Si(92 eV)Auger peak heights plotted against  $O_2$  exposure on caesiated  $Si(100)2 \times 1$  surfaces without and with steps, respectively, after various Cs exposures. A comparison of these figures with figure 1(b) suggests that the increasing coverage of pre-deposited Cs increases drastically the sticking coefficient and the maximum amount of O on the surface, in agreement with existing studies on metals and semiconductors [7, 8]. Figure 2 also shows that the presence of steps causes a further increase in the amount of adsorbed O for the same  $O_2$  exposure. According to [7], the adsorption of O on caesiated metal surfaces is controlled by the Cs overlayer. In the present case, the steps increase the Cs coverage which in turn causes further increase in the adsorbed O.

3.3.2. WF d measurements. Figure 3 shows the WF changes upon  $O_2$  adsorption on caesiated surfaces of Si(100)2 × 1 with steps. The broken curve shows the WF change on Cs deposition on a clean stepped Si(100)2 × 1 surface which has been discussed elsewhere [17]. The full circles show the WF change on  $O_2$  exposure starting at various levels of Cs deposition. The exposure increases along the x axis and the length of the arrow





Figure 3. WF changes on  $O_2$  adsorption on caesiated Si(100)2 × 1 with steps (for a Cs flux of  $1 \times 10^{12}$  cm<sup>-2</sup> s<sup>-1</sup>): ---, WF change on Cs adsorption on clean stepped Si(100)2 × 1;  $\bigoplus$ , WF change on  $O_2$  exposure, which increases along the x axis (the length of the arrow corresponds to 1 L).

corresponds to 1 L. This figure shows that, for Cs coverages up to the wF minimum  $\Phi_{min}$ , the adsorbed O causes an increase in the wF whereas, below  $\Phi_{min}$ , the wF initially decreases and subsequently increases with increasing O<sub>2</sub> exposure. Below  $\Phi_{min}$ , i.e. at low Cs coverages, O resides on both Cs-free areas and on top of Cs while, near saturation Cs coverage, O first resides underneath the Cs layer, causing lowering of the wF, and



**Figure 4.** WF change against annealing temperature of caesiated stepped Si(100)2 × 1 surfaces (which had been exposed to 30 L of O<sub>2</sub>) for various Cs deposition times (for a Cs flux of  $1 \times 10^{12} \text{ cm}^{-2} \text{ s}^{-1}$ ):  $\bigcirc$ , 130 s;  $\triangle$ , 300 s; ×, 500 s;  $\bigcirc$ , 1000 s.

subsequently on top of Cs. Similar results have been reported for  $O_2$  adsorption on caesiated metals and other semiconductors [6–9]. The results for  $O_2$  adsorption on caesiated Si(100)2 × 1 without steps (not shown) are similar with those in figure 3.

3.3.3. Heating effects. Figure 4 shows the change in WF plotted against annealing temperature of caesiated stepped Si(100)2 × 1 surfaces which were exposed to 30 L of O<sub>2</sub>. For all Cs coverages, the WF initially decreases and subsequently increases. According to the TDs measurements in § 3.3.4, neither Cs nor O<sub>2</sub> is removed from the surface at temperatures below 300 °C. However, the WF starts to decrease right above room temperature. This suggests that the O<sub>2</sub> which is adsorbed on top of the Cs layer diffuses under the Cs, thus causing the lowering of the WF. Above 300 °C, Cs is removed first and this removal is more drastic above 600 °C. The repetition of the above measurements on Si(100)2 × 1 without steps did not give any different results.

3.3.4. TDS measurements. Figures 5(a) and 5(b) show the TDS spectra of SiO and Cs from stepped and flat caesiated surfaces of Si(100)2 × 1, respectively, which had been exposed to 4 L of O<sub>2</sub>. The broken curves are the TDS spectra of a saturated Cs overlayer exposed to 4 L of O<sub>2</sub>. As the amount of pre-adsorbed Cs increases, the SiO peak increases. The desorption of the oxide from the surface takes place at  $T_s \ge 800$  °C, almost after complete removal of Cs. The important information from figure 5 is that the areas under the corresponding SiO peaks are greater on Si(100)2 × 1 with steps than on the same surface without steps, as shown more clearly in the next figure.

Figure 6 shows the areas under the TDS peaks of SiO (shown in figure 5) plotted against the time of Cs pre-deposition from the flat and stepped Si(100)2 × 1 surfaces which were exposed to 4 L of O<sub>2</sub>. The areas of SiO TDS peaks are proportional to the coverage of the oxide. According to previous reports [13, 19], similar TDS spectra of SiO have been attributed to a decomposition of SiO<sub>2</sub>, (SiO<sub>2</sub> + Si  $\rightarrow$  2SiO). This will be confirmed in § 3.4. Figure 6 shows that, for the same O<sub>2</sub> exposure, the amount of oxide increases almost linearly with increasing amount of pre-deposited Cs on both surfaces, with and without steps. This increase is greater on the stepped Si(100)2 × 1 surface. In





**Figure 5.** TDS spectra of SiO and Cs from (*a*) stepped and (*b*) flat caesiated surfaces of Si(100)2 × 1 (which had been exposed to 4 L of O<sub>2</sub>) for various Cs deposition times ( $\beta = 13 \text{ K s}^{-1}$ ; Cs flux,  $1 \times 10^{12} \text{ cm}^{-2} \text{ s}^{-1}$ ): curves A, 0 s; curves B, 100 s; curves C, 200 s; curves D, 300 s; curves E, 400 s; curves F, 500 s; curves G, 600 s; curves H, 800 s; curves I, 1200 s.

**Figure 6.** Areas under the TDs peaks of SiO (shown in figures 5(a) and 5(b)) against time of Cs pre-deposition from stepped ( $\odot$ ) and flat (×) Si(100)2 × 1 caesiated surfaces which had been exposed to 4 L of O<sub>2</sub> (for a Cs flux of  $1 \times 10^{12} \text{ cm}^{-2} \text{ s}^{-1}$ ).

other words, for the same  $O_2$  exposure, the amount of Si–O complex is greater on caesiated Si(100)2 × 1 with steps than without steps. The curves in figure 6 are analogous to those of the ratio of Cs to Si Auger peak heights against Cs deposition time on Si(100)2 × 1 with steps and without steps shown elsewhere [17]. This comparison implies that the amount of Si oxide on the surface depends on the coverage of pre-adsorbed Cs. The presence of steps increases the sticking coefficient and the maximum amount of Cs on Si(100)2 × 1 while the adsorption of  $O_2$  alone (§ 3.2) on clean Si(100)2 × 1 is not affected substantially by the steps.



Figure 7. Low-energy Auger spectra of Si(100)2 × 1 (*a*) with steps and (*b*) without steps after increasing caesiation, O<sub>2</sub> adsorption and subsequent heating to 800 °C. (Cs flux,  $1 \times 10^{12}$  cm<sup>-2</sup> s<sup>-1</sup>;  $E_p = 2$  kV; 4 L of O<sub>2</sub>).

#### 3.4. Identification of the Si oxides

Figures 7(a) and 7(b) show the low-energy Auger spectra of Si(100)2 × 1 surfaces without and with steps, respectively, on increasing degree of caesiation followed by exposure to  $O_2$  and heating to 800 °C. These figures show that, when the caesiated Si(100)2 × 1 surface is exposed to 15 L of  $O_2$  and subsequently heated to 800 °C for about 30 s, the 78 eV and 83 eV Si peaks appear, which are characteristic of SiO<sub>2</sub> and SiO<sub>4</sub>, respectively [2]. Most probably the formation of the oxide takes place during the heating cycle which provides the activation energy for the oxidation reaction. These peaks increase with increasing amount of pre-deposited Cs and are larger on Si(100)2 × 1 with steps than without steps. In other words, for the same time of Cs deposition and  $O_2$ exposure, more oxide is formed on Si(100)2 × 1 with steps than without steps. The results in figure 7 are consistent with attributing the observed TDS spectra of SiO to a decomposition of SiO<sub>2</sub>.

#### 3.5. Repetition of the oxidation cycle

Figures 8((a) and 8(b) show the low-energy Auger spectra after increasing number of cycles of O<sub>2</sub> adsorption on Cs saturated Si(100)2 × 1 surfaces without and with steps, respectively, and subsequent heating to 800 °C. As seen in these figures, the 78 eV peak due to SiO<sub>2</sub> increases drastically with increasing number of oxidation cycles. The increase is such that it masks the 83 eV peak due to SiO<sub>4</sub>, which remains almost invariant on repeating the number of oxidation cycles. This is consistent with previously reported results [2]. The important information in this figure is that the increase in the 78 eV peak, due to SiO<sub>2</sub>, with increasing number of oxidation cycles is substantially greater on the stepped surface, as shown better in the next figure.

Figure 9 shows the variation in the SiO<sub>2</sub> film thickness with increasing number of oxidation cycles (shown in figure 8) on Si(100)2  $\times$  1 with and without steps. After six



Figure 8. Low-energy Auger spectra after increasing number of cycles of room-temperature caesiation,  $O_2$  adsorption and heating to 800 °C of Si(100)2 × 1 surfaces (*a*) without and (*b*) with steps for various Cs deposition times: curves A, 0 s; curves B, 100 s; curves D, 300 s; curves F, 500 s.

Figure 9. Variation in the SiO<sub>2</sub> film thickness with increasing number of oxidation cycles (shown in figure 8) on Si(100)2  $\times$  1 with and without steps.

oxidation cycles the thickness d of the oxides was 16 Å and 11 Å on Si(100)2 × 1 with and without steps, respectively. The thickness d of the oxide was calculated from the following equation [2]:

$$I_{\rm Si} = I_{\rm Si}(0) \exp(-d/\lambda)$$

where  $I_{Si}(0)$  and  $I_{Si}$  are the Auger peak heights of Si(92 eV) from the clean surface and the oxidised surface, respectively, and  $\lambda$  is the mean free path of electrons in SiO<sub>2</sub> and has been taken to be equal to 6.5 Å [2]. From the slopes of the curves in figure 9, we may predict an increase in the oxide thickness with further increase in the number of oxidation cycles.

## 4. Conclusions

An experimental investigation of  $O_2$  adsorption on caesiated Si(100)2 × 1 surfaces with and without steps has been carried out in an UHV system by LEED, AES, TDS and WF measurements. The purpose of this experiment was to study the effect of the steps on the oxidation of  $Si(100)2 \times 1$  surfaces in the presence of Cs. The presence of Cs on both surfaces increases the sticking coefficient and the maximum amount of O2 that can be adsorbed on the surface. This increase is greater with increasing amount of pre-deposited Cs. At high Cs coverages, O is initially adsorbed under the Cs layer and subsequently resides on top of Cs. According to AES measurements, on exposure to  $O_2$  of a Si(100)2  $\times$  1 surface saturated with Cs and subsequent heating of the substrate to 800 °C, SiO<sub>4</sub> and  $SiO_2$  are formed on the surface. For convenience, we call this the oxidation cycle. WF and TDS measurements suggest that, on heating, the O which is adsorbed on top of Cs diffuses under the Cs layer and forms oxides with the substrate. At 800 °C, Cs is removed completely while the oxide remains. Above 800 °C, SiO<sub>4</sub> and SiO<sub>2</sub> dissociate and desorb in the form of SiO. After one oxidation cycle, the growth of the oxide is greater on stepped than on non-stepped Si(100)2  $\times$  1 surfaces. This is attributed to the higher saturation coverage of Cs on stepped Si. When the oxidation cycle is repeated several times, SiO<sub>2</sub> predominates and its average thickness increases with increasing number of oxidation cycles. For the same number of oxidation cycles, the thickness of SiO<sub>2</sub> is substantially greater on the stepped Si(100)2  $\times$  1. For example, after the sixth oxidation cycle the average thicknesses d of SiO<sub>2</sub> are about 16 Å and 11 Å on Si(100)2  $\times$  1 with and without steps, respectively.

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